

# KARPAGAVALLI

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## OBJECTIVE

Dynamic 7 years career reflecting pioneering experience in teaching and skilled performance in electronic circuit designing and embedded systems.

- Outstanding success in teaching and building strong base for student community.
- Exceptionally well organized with a track record that demonstrates self-motivation, creativity, and initiative to achieve organization goals.
- Expert in designing live power point presentations involving animations.
- Effective involvement in Microsoft Office tools.

## CAREER CHRONOLOGY

### **THE KINDER GARTEN STARTERS**

**Reporting to Supervisor**

#### **SUBSTITUTE TEACHER – KG.1**

**(October 2011 – till date)**

- Active contribution for KHDA inspection and constructed a “ZOO” model for the school.
- Substituted KG-1 classes with immense energy.
- Participated in “Snow ball” representing KG-1 and secured “Runner- Up”.
- Developing exclusive power point presentations for easy student understanding .
- Excellent involvement in “Art Exhibition”.
- Good free hand drawing skills and great interest in creativity/art.
- Technical skills on computer handling – created the KG-1 sports day invitation.

### **LOYOLA INSTITUTE OF TECHNOLOGY, INDIA**

#### **LECTURER**

**(July 2005 – Jan 2007)**

- Educate students in both individual and group sessions according to their needs.
- Complete comprehensive monthly reports of students for review.
- Regular power point presentations and seminars to enable communication skills.
- Regular meetings with the central staff and parents for assessments.

## **SRI PADMAVATHY COLLEGE OF ENGINEERING, INDIA**

### **LECTURER**

**(June 2004 – July 2005)**

- Complete comprehensive monthly reports of students for review.
- Conducted lectures and assisted lab sessions.
- Provided guidance in live projects for better practical exposure.
- Regular power point presentations and seminars to enable communication skills.

## **THE CMOSFT INFOTECH POINT, INDIA**

### **DESIGN ENGINEER / FACULTY**

**(July 2002 – June 2004)**

- Electronic circuit construction with CAD for PCB designing.
- Planned, directed, and coordinated activities of designated projects.
- Great exposure in PIC Micro Controllers (Embedded Systems)
- Guided real time projects for emerging engineers.
- Responsible for reviews and seminars on electronic circuit designing.
- Active participation in In-plant training for emerging engineers.
- Excelled in Power Point Presentations and Office tools.

## **ARIHANT E-SOFTLAB Ltd., INDIA**

### **DESIGN ENGINEER**

**(Sep 2000 – June 2002)**

- Involved in INTEL and PIC Micro Controller programming
- Educated trainees on the aspects of embedded systems and device drivers.
- PCB designing and soldering.
- Active participation in projects for engineers.

## **EDUCATION**

### **M. Tech in Applied Electronics**

2004 – 2007          Dr. M.G.R Educational and Research Institute , India

Acquired CGPA (Cumulative Grade Point Average) of 8.32 out of maximum 10

### **B.E. in Electrical and Electronics Engineering**

1996 – 2000          Sri Sairam Engineering College, India

Secured first class (from University of Madras)

## **TECHNICAL KNOWLEDGE**

Assembly language programming : 8085, 8051, PIC microcontroller  
Office Applications : MS Office, Outlook  
Designing software : Adobe Photoshop  
Software : C for real-time programming and PIC programming for  
embedded systems  
Electronics : Digital circuit designing

## **PERSONAL INFORMATION**

Date of Birth : Nov 18th 1978  
Nationality : Indian  
Marital Status : Married  
Sex : Female  
Visa Status : Residence Visa  
Passport No. : G2394620  
Languages : English, Tamil and Malayalam

